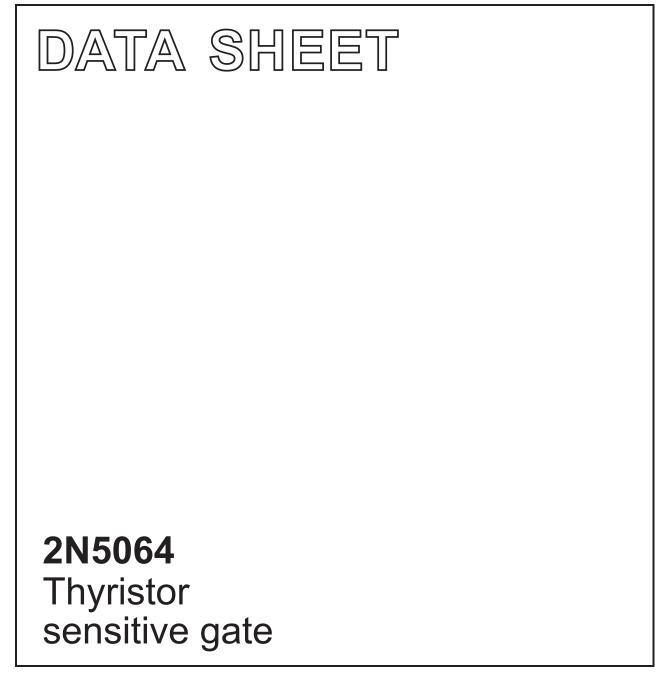
DISCRETE SEMICONDUCTORS



Product specification

October 1997



Thyristor sensitive gate

Product specification

2N5064

GENERAL DESCRIPTION

Glass passivated sensitive gate thyristor in a plastic envelope, intended for use in general purpose switching and phase control applications. This device is intended to be interfaced directly to microcontrollers, logic integreated circuits and other low power gate trigger circuits.

PINNING - TO92 variant

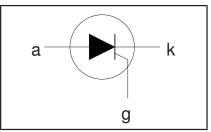
PIN	DESCRIPTION	
1	anode	
2	gate	
3	cathode	

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{DRM} ,	Repetitive peak off-state voltages	200	V
V _{RRM} I _{T(AV)} I _{T(RMS)} I _{TSM}	Average on-state current RMS on-state current Non-repetitive peak on-state current	0.5 0.8 10	A A A

PIN CONFIGURATION

SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
$V_{\text{DRM}}, V_{\text{RRM}}$	Repetitive peak off-state voltages		-	200	V
I _{T(AV)}	Average on-state current	half sine wave $T_c \le 67 \degree C$ $T_c \le 102 \degree C$	-	0.51 0.255	A
I _{T(RMS)} I _{TRM}	RMS on-state current Repetitive peak on-state current	all conduction angles	-	0.8	A A
I _{TSM}	Non-repetitive peak on-state current	half sine wave; T _a = 25 °C prior to surge; t = 8.3 ms	-	10	А
l ² t	I ² t for fusing	t = 8.3 ms	-	0.4	A ² s
I _{GM} V _{GM}	Peak gate current	T _a = 25°C, t _p = 300μs; f = 120 Hz	-	1	A
V _{GM} V _{RGM}	Peak gate voltage Peak reverse gate voltage		-	5 5	V V
P _{GM}	Peak gate power	$T_a = 25^{\circ}C$	-	0.1	W
$\begin{array}{c} P_{G(AV)} \\ T_{stg} \\ T_{j} \end{array}$	Average gate power Storage temperature Operating junction temperature	$T_a^{"} = 25^{\circ}C$, over any 16 ms period	-65 -65	0.01 150 125	Û. Û

Thyristor sensitive gate

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-c}	Thermal resistance junction to case	see note:1	-	-	75	K/W
R _{th j-a}	Thermal resistance junction to ambient		-	200	-	K/W

STATIC CHARACTERISTICS

 $T_c = 25$ °C, $R_{GK} = 1 \text{ k}\Omega$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{GT}	Gate trigger current	$T_c = 25 °C$ $T_c = -65 °C$	-	-	200 350	μΑ
		$V_{\rm D} = V_{\rm DRM(max)}; R_{\rm L} = 100 \ \Omega; \text{ gate open}$	-	-	350	μA
IL IL	Latching current	$V_{D} = 12 \text{ V}; $	-	-	6	mA
l I _H	Holding current	$V_{\rm D} = 12 \text{ V}; \text{ R}_{\rm GK} = 1 \text{ k}\Omega$	-	-	5	mA
I _H V _T	On-state voltage	$I_T = 1.2 \text{ A peak}; t_p = 300 \ \mu\text{s}; \delta \le 0.01$	-	-	1.7	V
V _{GT}	Gate trigger voltage	$T_{i} = 25 °C$	-	-	0.8	V
		$ T_{i} = -65 \ ^{\circ}C$	-	-	1.2	V
		T _i = 125 °C	0.1	-	-	V
		$V_{D}^{i} = V_{DRM(max)}; R_{L} = 100 \Omega; gate open circuit$				
	Off-state leakage current					
I _D , I _R	Chi-state leakage current		-	-	10	μA
		T _j = 125 °C	-	-	50	μA

DYNAMIC CHARACTERISTICS

 T_{c} = 25 °C, R_{GK} = 1 k Ω unless otherwise stated

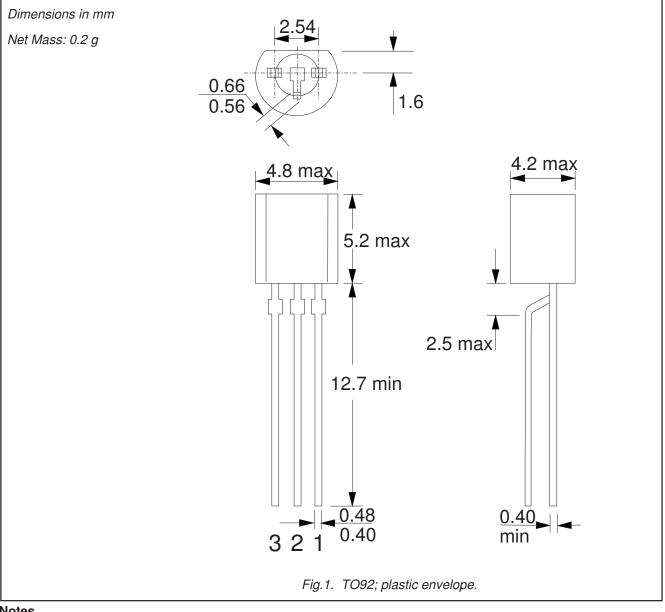
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV _D /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125 °C;$ exponential waveform; $R_{GK} = 1 k\Omega$	-	25	-	V/µs
t _{gt}	Gate controlled turn-on time	$I_{TM} = 2 \text{ A}; V_D = V_{DRM(max)}; I_G = 10 \text{ mA};$ $dI_G/dt = 0.1 \text{ A}/\mu\text{s}$	-	2	-	μs
t _q	Circuit commutated turn-off time	$ \begin{array}{l} V_{\text{DM}} = 67\% \; V_{\text{DRM(max)}}; \; T_{\text{j}} = 125 \; ^{\circ}\text{C}; \\ I_{\text{TM}} = 1.6 \; \text{A}; \; V_{\text{R}} = 35 \; \text{V}; \; \text{d}I_{\text{TM}}/\text{d}t = 30 \; \text{A}/\mu\text{s}; \\ \text{d}V_{\text{D}}/\text{d}t = 2 \; \text{V}/\mu\text{s}; \; \text{R}_{\text{GK}} = 1 \; \text{k}\Omega \end{array} $	-	100	-	μs

¹ This measurement is made with the case mounted "flat side down" on a heatsink and held in position by means of a metal clamp over the curved surface.

Thyristor sensitive gate

2N5064

MECHANICAL DATA



Notes 1. Epoxy meets UL94 V0 at 1/8".

Legal information

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

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Contact information

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